

PRIOR ART

Fig. 1

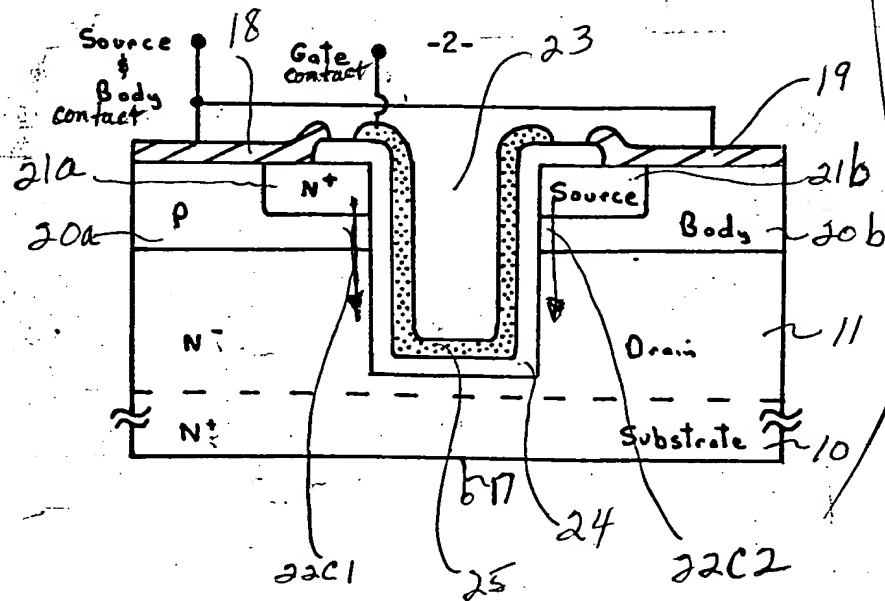


Fig. 2

PRIOR ART

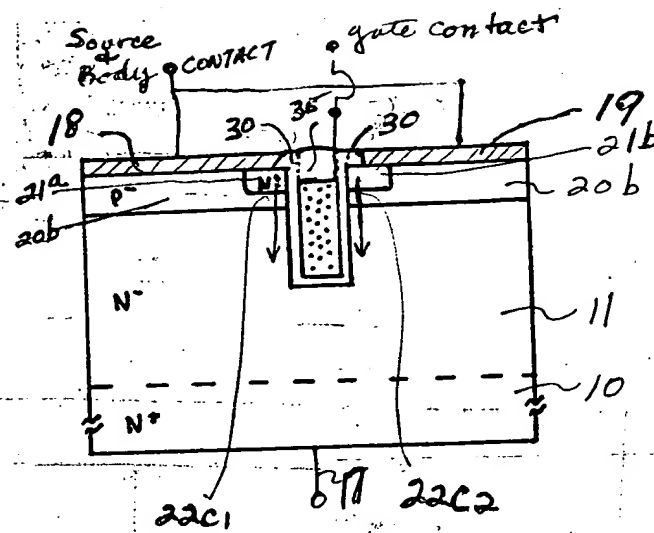


Fig. 3

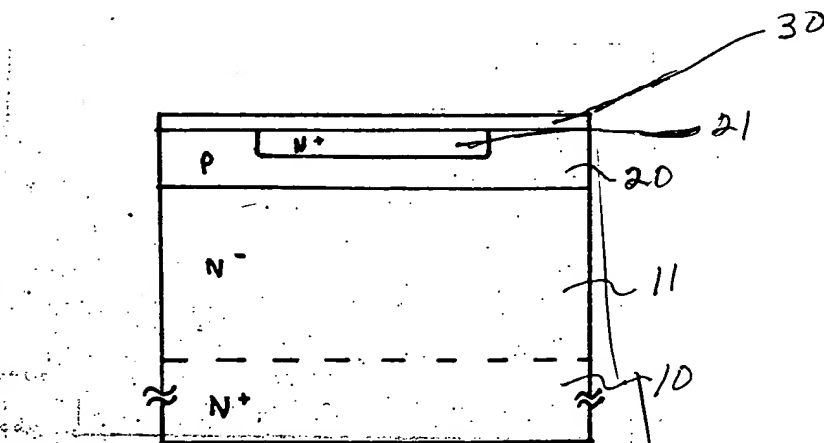


Fig. 4a

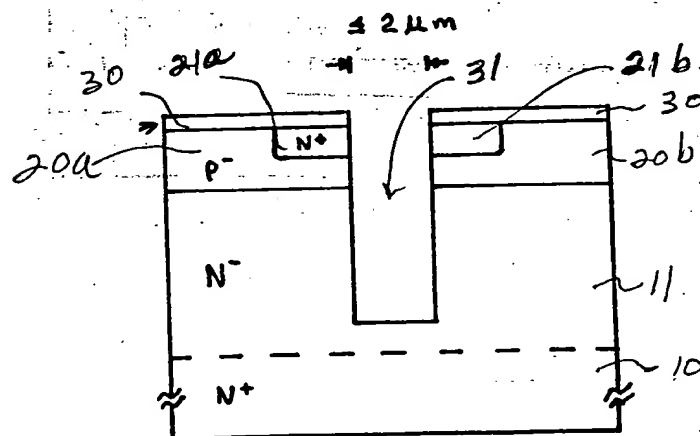


Fig. 4.6

M-300

Fig 4d

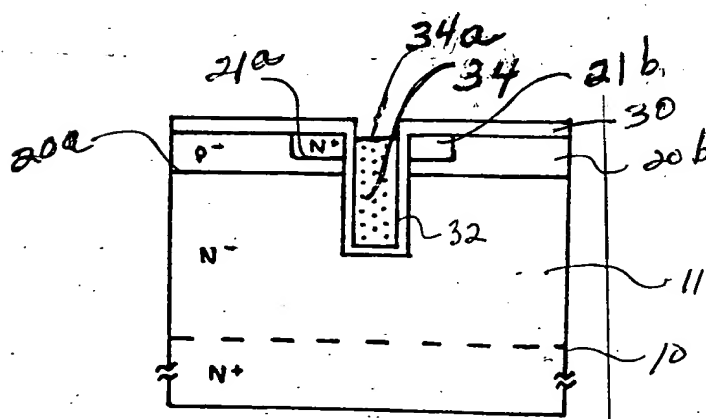


Fig 4e

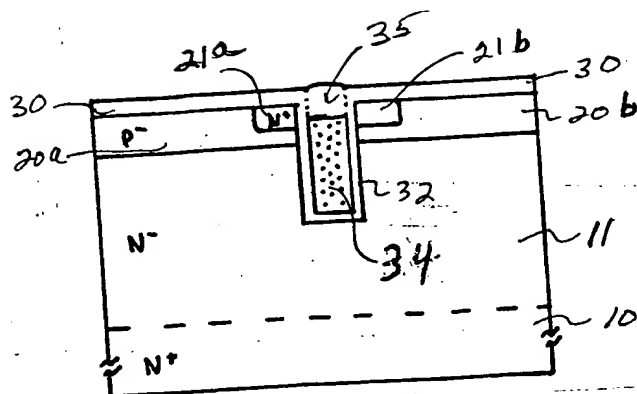
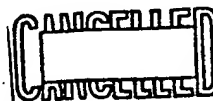


Fig. 4f

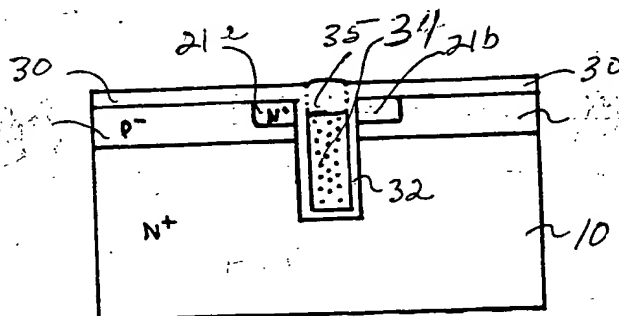


Fig. 5

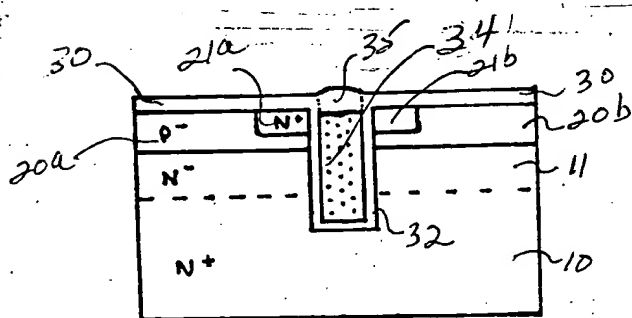


Fig. 6

M-300

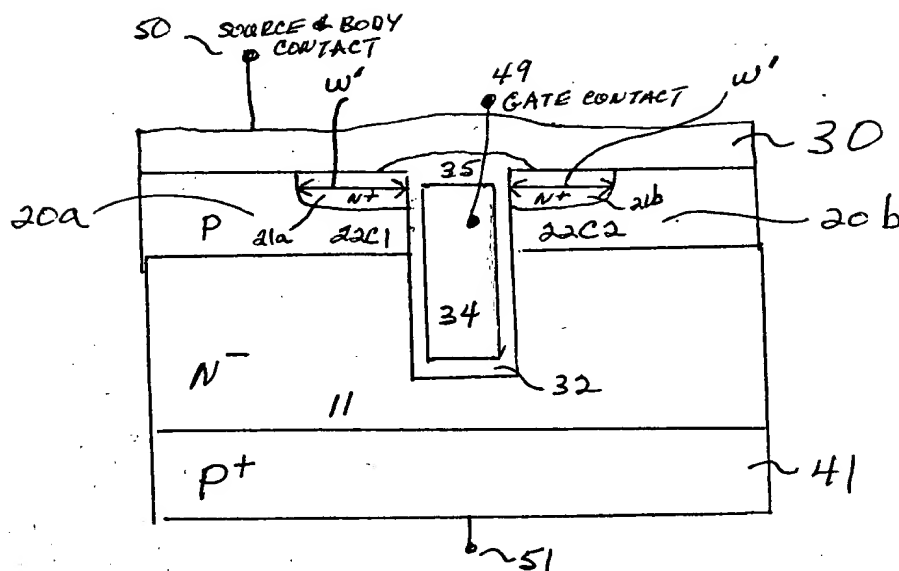


Fig. 7 Cross-section of an insulated gate transistor

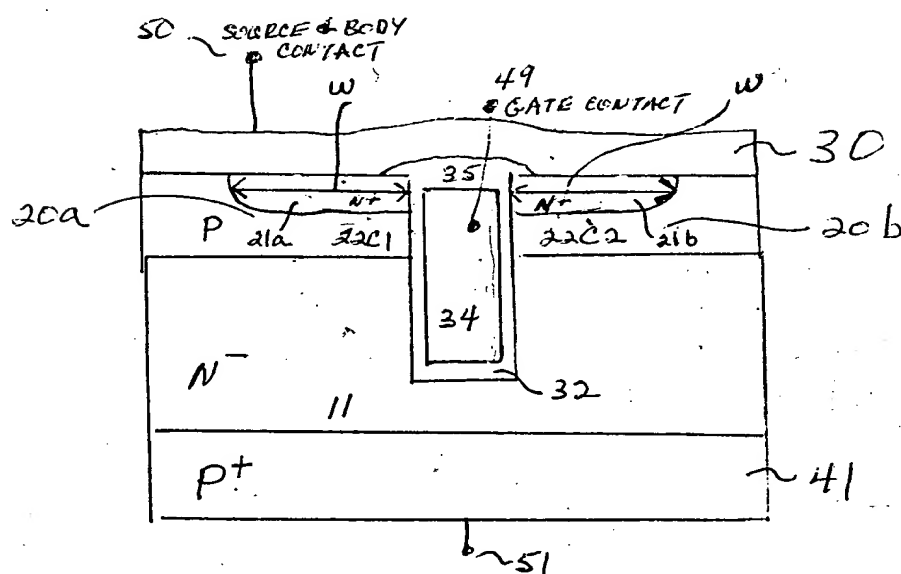


Fig. 8 Cross-section of a MOS-gated SCR

CANCELLED

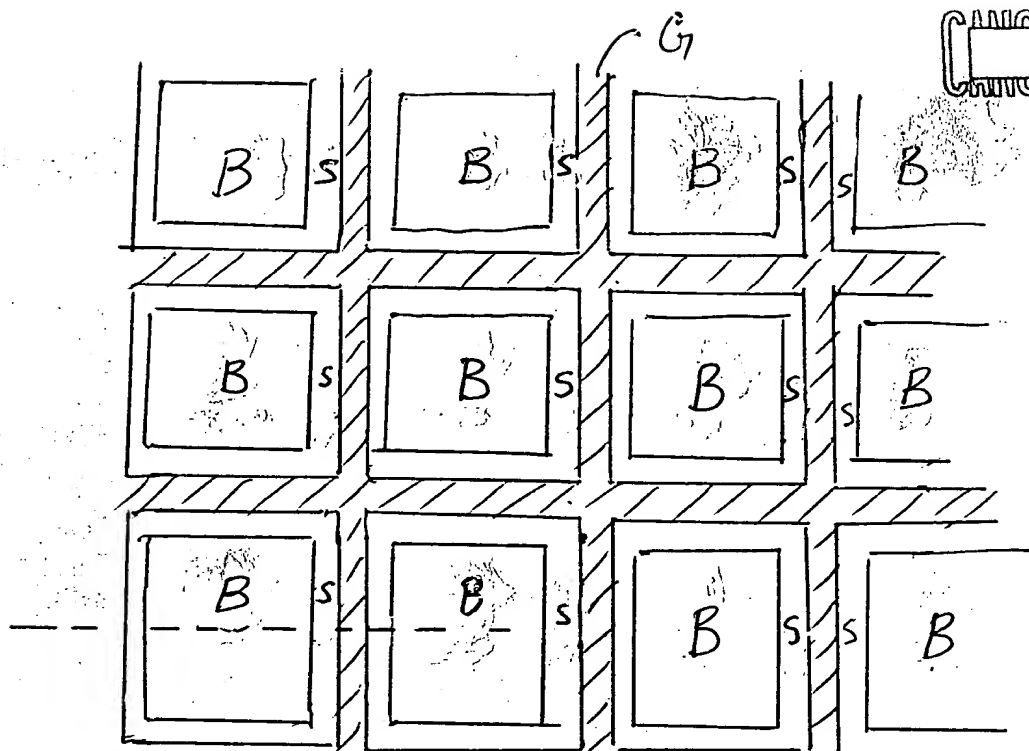


Fig. 9